

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,189,431 B2
APPLICATION NO. : 10/711717
DATED : March 13, 2007
INVENTOR(S) : Yamasaki et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 2, Line 12, "thereby inhibiting" should read --thereby inhibit--.
Col. 8, Line 28, " C_3H_5OH ," should read -- C_2H_5OH --.
Col. 9, Line 28, "module" should read --nodule--.
Col. 10, Line 37, "arrays (FPGAs), (not shown)" should read --arrays (FPGAs)), (not shown)--.
Col. 12, Line 35, "maybe" should read --may be--.
Col. 12, Line 54, "assistant (PDA) laptop" should read --assistant (PDA), laptop--.
Col. 12, Line 58, "configured to depositing" should read --configured to deposit--.
Col. 14, Line 7, " C_2C_4 " should read -- C_2H_4 --.

Signed and Sealed this

Twenty-fifth Day of December, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office